## Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

## Listing of Claims:

1. (currently amended) A mold die comprising a first die having a recess in-of a predetermined form and a second flat die, said mold die being for disposing-said first die to be disposed on a surface of a wiring board which has a plurality of openings and bears-a semiconductor chip mounted on said surface via an elastic material, which surface bears said semiconductor-chip, and for disposing-said second die to be disposed on a back of said surface of said wiring board on which bears-said semiconductor chip is mounted, and for sealing with an insulating resin a periphery of said semiconductor chip and at least one of said openings of said wiring board, wherein

said second die comprises a protrusion <u>disposed</u> around an area overlapping said opening <u>to be</u> sealed with said insulating resin.

2. (currently amended) A method for manufacturing a semiconductor device by sealing, by transfer mold <u>processing</u> using a die, a semiconductor chip <del>borne</del> mounted on a wiring board via an elastic material, which board includes an insulating substrate with a plurality of openings thereon on which a conductive pattern is formed, and by sealing at least one of said openings, wherein

a die having a protrusion <u>disposed</u> around an area overlapping said <u>sealed</u> opening <u>to be sealed</u> is used for a back <u>die member to be placed in contact with of</u> the surface of said wiring board <u>on the opposite side on</u> which <u>bears</u> said semiconductor chip <u>is mounted</u>.